

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/792,112	ASLAN ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	John H. Le	2863	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Applicant's amendment filed 02/02/2006.
2. ☒ The allowed claim(s) is/are 1-24.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |  |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|   | 9. <input type="checkbox"/> Other _____.   |

***Response to Amendment***

1. Applicant's amendment filed 02/02/2006 has been entered and carefully considered.

Claims 21-24 have been amended.

***Reasons for Allowance***

2. Claims 1-24 are allowed.
3. The following is a statement of reasons for the indication of allowable subject matter:

Please see the previous office action and applicant's argument filed on 02/02/2006 and 10/26/2005.

Regarding claims 1, 4, 6, 22, and 23, none of the prior art of record teaches or suggests the combination of a temperature measurement circuit, comprising: two signal channels that are configured to receive, at first and second sense nodes, a differential input signal from a dual Junction temperature sensor circuit, and further configured to provide a differential intermediate signal from the differential input signal; a conversion circuit that is configured to convert the differential intermediate signal into a digital temperature signal that is associated with a remote temperature. It is these limitations as they are claimed in the combination with other limitations of claim, which have not been found, taught or suggested in the prior art of record, that make these claims allowable over the prior art.

Regarding claim 11, none of the prior art of record teaches or suggests the combination of a temperature measurement circuit, comprising a sensing circuit that is

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configured to receive, at first and second sense nodes, a differential input signal from a dual junction temperature sensor circuit, and further configured to provide a differential intermediate signal from the differential input signal, wherein the sensing circuit includes: a first current source circuit that is configured to provide a first bias current, wherein the first bias current corresponds to one of a first value if the current control signal corresponds to a deasserted value, and corresponds to a second value if the current control signal corresponds to an asserted value; a second current source circuit that is configured to provide a second bias current such that the second bias current corresponds to a third value if the current control signal corresponds to the deasserted value, and corresponds to a fourth value if the current control signal corresponds to an asserted value. It is these limitations as they are claimed in the combination with other limitations of claim, which have not been found, taught or suggested in the prior art of record, that make these claims allowable over the prior art.

Regarding claim 20, none of the prior art of record teaches or suggests the combination of a temperature measurement circuit, comprising means for providing a differential intermediate signal from a differential input signal, wherein the means for providing includes: means for providing a first bias current; and means for providing a second bias current; means for converting the differential intermediate signal into a temperature signal that is related to a temperature of a remote device such that the temperature of the remote device is calculated based on a voltage difference between two pn junctions. It is these limitations as they are claimed in the combination with other

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limitations of claim, which have not been found, taught or suggested in the prior art of record, that make these claims allowable over the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

**Contact Information**

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to John H. Le whose telephone number is 571 272 2275. The examiner can normally be reached on 9:00 - 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John E. Barlow can be reached on 571 272 2269. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.


Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



John H. Le

Patent Examiner-Group 2863

February 17, 2006



John Barlow  
Supervisory Patent Examiner  
Technology Center 2800